

WEST Search History

DATE: Sunday, October 05, 2003

<u>Set</u> <u>Name</u> side by side	<u>Query</u>	<u>Hit</u> <u>Count</u>	<u>Set</u> <u>Name</u> result set
	<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
L13	(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or exerci\$4 or stimul\$7) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same (vector or pattern or sequence) same compar\$4 near2 (cell or module or processor) same program same function	18	L13
L12	111 and (test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or exerci\$4 or stimul\$7) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same compar\$4 same (vector or pattern or sequence) same (cell or module) same program same function	93	L12
L11	110 and (test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same compar\$4 same (vector or pattern or sequence) same (cell or module) same program	457	L11
L10	(test\$3 or inspect\$4 or check\$4 or debug\$4 or verif\$7 or diagno\$5 or probing or probe or exerci\$4 or stimul\$7) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same compar\$4 same (vector or pattern or sequence)	21538	L10
L9	L7 and l5	1	L9
L8	L7 and l5	1	L8
L7	12 and (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux) with control\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same decod\$3 near2 (single or unique or "only one" or singular) same input\$4 same ouput\$4	1	L7
L6	('4654573')[ABPN1,NRPN,PN,TBAN,WKU]	2	L6
L5	12 and (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux) with control\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same decod\$3 near2 (single or unique or "only one" or singular) same input\$4 same ouput\$4 and (program\$7 or adjust\$6 or vari\$4 or vary\$4 or configur\$7 or re\$1configur\$7 or re\$1defin\$6 or chang\$7 OR ALTER\$7)	1	L5
	12 and (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux) with control\$4 same (ic or (integrat\$3 near2 circuit\$3) or		

L4	semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same decod\$3 near2 (single or unique or "only one" or singular) same input\$4 same ouput\$4 same (program\$7 or adjust\$6 or vari\$4 or vary\$4 or configur\$7 or re\$1configur\$7 or re\$1defin\$6 or chang\$7 OR ALTER\$7)	0	L4
L3	l2 and (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux) with control\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same decod\$3 near2 (single or unique or "only one" or singular) same input\$4 same ouput\$4 same (configur\$4 or adapt\$4 or capab\$7)	0	L3
L2	l1 and (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux) with control\$4 same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same decod\$3 near2 (single or unique or "only one" or singular)	267	L2
L1	(switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux) same (ic or (integrat\$3 near2 circuit\$3) or semi\$1conduct\$7 or semi conduct\$7 or memor\$7 or stor\$4 or register\$4 or array) same control\$4 same decod\$3 near2 (single or unique or "only one" or singular)	517	L1

END OF SEARCH HISTORY